## Amendments to the Specification:

Please amend the specification as follows:

Amend the specification at the paragraph at page 9, line 28 - page 10, line 3, as follows: Referring to FIG. 4, complementary cycle encoding circuit 154 provides a CCES which is driven by driver 124 to interconnect 24B and receiver 180. Cycle Complementary cycle encoding circuit 154 includes delay and invert circuit 172, delay circuit 174, delay, divide and invert circuit 176, and delay and divide circuit 178 to produce signals SF\*, SF, SF/2\*, and SF/2, respectively, which are the inverses of SF, SF\*, SF/2, and SF/2\* produced by cycle encoding circuit 152.